

ABSTRACT OF THE DISCLOSURE

Disclosed is a driving circuit for driving a capacitive load promptly to a target voltage. The driving circuit is to have a broad
5 dynamic range and is to achieve a high accuracy output and saving in the surface area with low power dissipation. A first period and a second period are provided in one data driving period. During the first period, a transistor amplifier for driving the load for charging, with a setting drive voltage (V1), and a transistor amplifier for driving the load for
10 discharging, with a setting drive voltage (V2), with $V1 < V2$, are both enabled for actuation and, during the second period, the transistor amplifier performing either the driving for charging or the driving for discharging, and a constant current source, performing the reverse of the operation of the transistor amplifier, are actuated, for driving the load to
15 the target voltage. This achieves a broad dynamic range, high-speed driving, high accuracy output and saving in the surface area with low power dissipation.